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Electron Devices, IEEE Transactions on , Volume: 49 Issue: 5 , May 2002
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Pierdomenico, J.; Wurcer, S.; Day, B.

Solid-State Circuits Conference, 2002. Digest of Technical Papers. ISSCC. 2002.
International , Volume: 1 , 2002
Page(s): 320 -470 vol.1

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3 **A 1.2Gbps SOI-BiCMOS write driver for hard disk drives**

Yoshizawa, H.; Kobayashi, Y.; Yoshinaga, M.; Ookuma, Y.; Maio, K.; Inkura, I.
Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002
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4 **A low loss high-frequency half-bridge driver with integrated power using EZ-HV SOI technology**

Li, F.; Giannopoulos, D.; Wacyk, I.

Applied Power Electronics Conference and Exposition, 2002. APEC 2002. Seventh Annual IEEE , Volume: 2 , 2002

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Page(s): 3153 -3156 vol.5[\[Abstract\]](#) [\[PDF Full-Text \(416 KB\)\]](#) **CNF****3 A three-dimensional high-throughput architecture using through-optical interconnect***Wills, D.S.; Lacy, W.S.; Camperi-Ginestet, C.; Buchanan, B.; Cat, H.H.; Wilk Lee, M.; Jokerst, N.M.; Brooke, M.A.*Lightwave Technology, Journal of , Volume: 13 Issue: 6 , June 1995
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